

**AMENDMENTS TO THE DRAWINGS**

The attached sheets of drawings correct typographical errors and/or improve clarity in FIG. 3.

Attachments:      One (1) Replacement Sheet (FIG. 3)  
                        One (1) Annotated Sheet (FIG. 3)

**REMARKS/ARGUMENTS**

In this Amendment Under 37 C.F.R. § 1.111 (“Amendment”), Applicants cancel without prejudice or disclaimer, claims 16-24. Applicants also amend paragraphs [0001], [0011], [0024], and [0034] of the specification in order to correct typographical errors and/or improve clarity; amend the drawings in order to correct typographical errors and/or improve clarity in FIG. 3; and amend claims 1-3, 5, 6, 8, 9, and 11-15 in order to correct typographical errors and/or improve clarity. No new matter is introduced.

Prior to entry of the Amendment, claims 1-24 were pending in the application. After entry of the Amendment, claims 1-15 are pending in the application.

In the Office Action, the Examiner rejected claims 16, 17, and 21 under 35 U.S.C. § 102(a) as being anticipated by alleged admitted prior art (“the AAPA”); and rejected claims 18-20 and 22-24 under 35 U.S.C. § 103(a) as being unpatentable over the AAPA in view of U.S. Patent No. 6,067,329 to Kato et al. (“Kato”). Applicants respectfully traverse the rejections under 35 U.S.C. § 102(a) and 35 U.S.C. § 103(a).

The Examiner also stated that claims 1-15 are allowed. Applicants gratefully acknowledge the Examiner’s statement that claims 1-15 are allowed.

**Allowed Claims**

In allowing claims 1-15, the Examiner indicated that as-filed, independent claims 1 and 9 recited “a matched filter for filtering the first and

second baseband signals to control signal-to-noise ratios thereof”; “a sort circuit for shifting the frequencies of outputs from the matched filter”; “a direct current removal circuit that combines the outputs of the sort circuit and removes a direct current component from the result of combination”; “a sampling rate control circuit for changing the sampling rate of an output of the DC removal circuit and for outputting the result”; and “a [symbol] timing restoration circuit for measuring a timing error in related symbols of the output of the DC removal circuit and for generating the address selection signal that is proportional to the timing error, in response to a carrier restoration signal that is generated by the carrier restoration circuit and for indicating restoration of the carrier”. Office Action, p. 5 (emphases omitted).

Applicants note that as-filed, independent claim 9 does not include those recitations. However, Applicants understand the Examiner’s statement to mean that claim 9 is allowed because the references of record, either singly or in combination, fail to anticipate or render obvious “receiving the first and second baseband signals, controlling their SNRs, and shifting the frequency bands of the SNR-controlled first and second baseband signals”; “combining the frequency-shifted first and second baseband signals and removing a DC component from a signal obtained therefrom”; “changing and outputting the sampling rate of the signal obtained in the combining step”; “receiving the first and second baseband signals, detecting the frequency offset from the carrier, and generating the complex sinewave that is proportional to the frequency

offset"; and "receiving the signal obtained in the combining step and generating the address selection signal, in response to a carrier restoration signal that indicates restoration of the carrier".

Amendments to the Specification

As discussed above, Applicants amend paragraphs [0001], [0011], [0024], and [0034] of the specification in order to correct typographical errors and/or improve clarity.

Amendments to the Claims

Additionally, as discussed above, Applicants amend claims 1-3, 5, 6, 8, 9, and 11-15 in order to correct typographical errors and/or improve clarity.

Amendments to the Drawings

As also discussed above, Applicants amend the drawings in order to correct typographical errors and/or improve clarity in FIG. 3.

Status of the Drawings

Applicants note that the Form PTOL-326 in the Office Action does not indicate the status of the drawings filed on January 23, 2004.

As a result, Applicants request that the Examiner indicate the status of the drawings—including those filed with this Amendment—in the next paper mailed by the U.S. Patent and Trademark Office ("USPTO").

Request for Reconsideration and Allowance

Accordingly, in view of the above amendments and remarks, reconsideration of the rejections and allowance of each of claims 1-15 in

connection with the present application is earnestly solicited.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact the undersigned at the telephone number listed below.

If necessary, the Director of the USPTO is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; in particular, extension of time fees.

Respectfully submitted,

HARNESS, Dickey, & PIERCE, P.L.C.

By \_\_\_\_\_

John A. Castellano Reg. No. 35,094

P.O. Box 8910  
Reston, VA 20195  
703.668.8000

JAC/LFG:hcw

FIG. 3 (CONVENTIONAL)

